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**ABSTRACT**

The invention concerns a clock-generating circuit, which generates dot clock pulses for driving a light-emitting element employed in an optical-writing section of an image-forming apparatus and has a function of canceling unevenness of scanning-light amount caused by variations of transmittances and reflectances in the optical-writing section when a polygon mirror scans a laser beam. The clock-generating circuit includes a digital-delay dot clock adjusting section to adjust timings of rising-edges or falling-edges of the dot clock pulses generated by changing a selection for a plurality of delayed-clock pulses, which are generated by delaying clock-pulses, outputted from a reference oscillator, in slightly different delay times; and a controlling section to control a selecting operation for the plurality of delayed clock pulses, performed in the digital-delay dot clock adjusting section, so as to compensate for unevenness of scanning-light amount caused by the variations of transmittances and reflectances in the optical-writing section.